



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Kinsman et. al.

**Serial No.:** 10/624,833

**Filed:** July 22, 2003

**For:** METHOD FOR FABRICATING A  
CHIP SCALE PACKAGE USING WAFER  
LEVEL PROCESSING AND DEVICES  
RESULTING THEREFROM

**Confirmation No.:** Unknown

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 2269-3572.2US  
(97-1243.02/US)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 17, 2003  
Date

Signature

Leah J. Barrow  
Name (Type/Print)

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a). The listed documents were cited by the Office in co-pending application Serial No. 09/917,127, filed on July 27, 2001, and directed to a related invention.

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

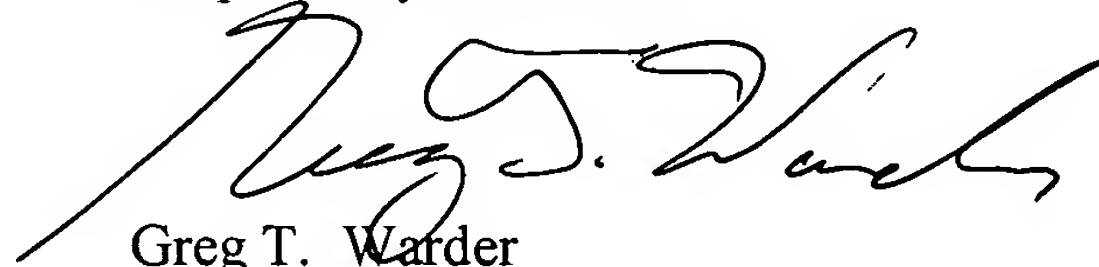
U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 6,331,450	12/2001	Uemura

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits.

Respectfully submitted,



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Date: November 17, 2003

GTW/dlm:ljb

Enclosures: Form PTO-1449 or PTO/SB/08  
Copy of documents cited

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Substitute for form 149A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(use as many sheets as necessary)*

Sheet

1

of

1

**Complete if Known**

Application Number

10/624,833

Filing Date

July 22, 2003

**First Named Inventor**

Kinsman et. al.

### Group Art Unit

Unknown

Examiner Name

Unknown

Attorney Docket Number

2269-3572.2US (97-1243.02/US)

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

Examiner  
Signature

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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